CLAIMS

We claim:

1. A method of tuning a system comprising:
tuning a receiver to a first frequency;
receiving a first field of video associated with the first frequency;
tuning a receiver to a second frequency;
receiving a second field of video associated with the second frequency, wherein the first field of video and the second field of video are adjacent in time;

2. The method of claim 1, wherein the step of tuning the receiver to a second frequency further comprises tuning the receiver to the second frequency during a vertical blanking interval.

3. The method of claim 1 further comprising:
providing a second frequency indicator to the receiver prior to the step of tuning the receiver to a second frequency.

4. The method of claim 3, wherein the step of providing includes providing the second frequency indicator in less than approximately 1.2 milliseconds.

5. The method of claim 1 further comprising the steps of :
displaying the first field;
tuning the receiver to the first frequency after the step of receiving the second field;
receiving a third field associated with the first frequency;
displaying the third field, wherein the first field and the third field are adjacent frames of a common video image.

6. The method of claim 1, wherein the first and second fields of video are adjacent when no fields of video are transmitted at the second frequency after a last data of the first field of video and before the first data of the second field of video.

7. The method of claim 1, wherein the step of tuning the receiver to a second frequency occurs during a vertical blanking interval.

8. A method of providing video, the method comprising:

tuning a receiver to a first frequency;

receiving a first field of video associated with the first frequency;

tuning a receiver to a second frequency;

receiving a second field of video associated with the second frequency, wherein the first field of video and the second field of video are adjacent in time;

tuning the receiver to the first frequency;

receiving a third field of video associated with the first frequency;

displaying an image based upon the first field at a first location of a display device;

displaying an image based upon the second field at a second location of a display device, wherein the first location and the second location are substantially mutually exclusive; and

displaying an image based upon the third field at the first location of the display device to provide a full motion video sequence.

9. A method of displaying video, the method comprising:

alternating reception of a first field set and a second field set at a common receiver, wherein the first field set is associated with a first frequency, and the second frame set is associated with a second frequency; and

simultaneously displaying the first field set and the second field set as full motion video.

10. The method of claim 9, wherein the step of alternating includes alternating reception of a first field set and a second field set at a common receiver in approximately 1.2 milliseconds.

11. The method of claim 9, wherein the step of simultaneously displaying includes simultaneously displaying the first field set and the second frame set as full motion video on a single display device.

12. The method of claim 9, wherein the step of simultaneously displaying includes simultaneously displaying the first field set and the second frame set as full motion video on different display devices.

13. A system comprising:

a first prelude register having an output;

a first control register coupled to the output of the first prelude register, the first control register having an output ;

a second prelude register having an output;

a second control register coupled to the output of the first prelude register, the second control register having an output ;

a first oscillator coupled to the output of the first control register, the first oscillator to provide an output signal;

a second oscillator coupled to the output of the second control register, the second oscillator to provide an output signal ;

a first mixer to coupled to an antenna and to receive the output of the first oscillator and to provide an output signal; and

a second mixer coupled to receive the output signal from the first mixer and the output signal from the second oscillator, and to provide an output signal.

14. The system of claim 13 further comprising:

an analog-to-digital converter coupled to receive the output signal from the second mixer and providing a digital output; and

a video decoder coupled to receive the digital output from the analog-to-digital converter, and to provide a digital video output.

15. The system of claim 14 further comprising:

a display engine coupled to receive the digital video output to a display device.

16. The system of claim 13, further comprising:

a memory coupled to receive the digital video output.

17. The system of claim 13, wherein the memory includes a cache memory.

18. The system of claim 13, wherein the memory includes a video memory.

19. The system of claim 13, wherein the memory includes a system memory.

20. A system comprising:

a first register having an output;

a second register having an output;

a third register having an output;

a fourth register having an output;

a first multiplexor having a first input coupled to the output of the first register, a second input coupled to the output of the second register, a select input, and an output;

a second multiplexor having a first input coupled to the output of the third register, a second input coupled to the output of the fourth register, a select input, and an output;

a first oscillator coupled to the output of the first multiplexor, the first oscillator to provide an output signal;

a second oscillator coupled to the output of the second multiplexor, the second oscillator to provide an output signal ;

a first mixer to coupled to an antenna and to receive the output of the first oscillator and to provide an output signal; and

a second mixer coupled to receive the output signal from the first mixer and the output signal from the second oscillator, and to provide an output signal.